Post-Silicon and Runtime Verification for Modern Processors

By Ilya Wagner, Valeria Bertacco

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The purpose of this book is to survey the state of the art and evolving directions in post-silicon and runtime verification. The authors start by giving an overview of the state of the art in verification, particularly current post-silicon methodologies in use in the industry, both for the domain of processor pipeline design and for memory subsystems. They then dive into the presentation of several new post-silicon verification solutions aimed at boosting the verification coverage of modern processors, dedicating several chapters to this topic. The presentation of runtime verification solutions follows a similar approach. This is an area of processor design that is still in its early stages of exploration and that holds the promise of accomplishing the ultimate goal of achieving complete correctness guarantees for microprocessor-based
computation. The authors conclude the book with a look towards the future of late-stage verification and its growing role in the processor life-cycle.

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Editorial Review

From the Back Cover

Post-Silicon and Run-Time Verification for Modern Processors surveys the state of the art and evolving directions in post-silicon and runtime verification. The volume gives an overview of the state of the art in verification, particularly current post-silicon methodologies in use in the industry, both for the domain of processor pipeline design and for memory subsystems. There is also a thorough presentation of several new post-silicon verification solutions aimed at boosting the verification coverage of modern processors. The presentation of runtime verification solutions follows a similar approach. This is an area of processor design that is still in its early stages of exploration and could help accomplish the ultimate goal of complete correctness guarantees for microprocessor-based computation. The book also:

·   Addresses an area of hardware verification that is growing both in industry and academia

·   Covers hardware patching and error avoidance

·   Discusses multi-core processors with test generation and response evaluation

The authors conclude the book with a look towards the future of late-stage verification and its growing role in the processor life-cycle. *Post-Silicon and Run-Time Verification for Modern Processors* will be a valuable book for researchers and engineers working in electrical engineering.

Users Review

From reader reviews:

Lucille Davis:

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